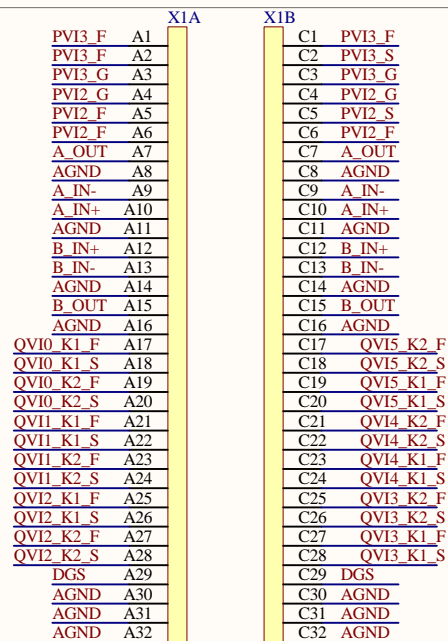
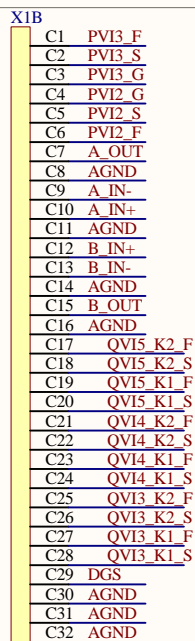


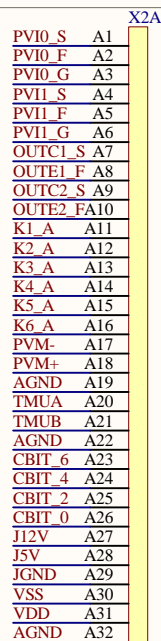
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C	<div>U_OPL1 OPL1.SchDoc</div> <div></div>	<div>U_OPL2 OPL2.SchDoc</div> <div></div>	
D			<div>Title</div> <div><div>Size A4</div><div>Number</div><div>Revision 1.06</div></div> <div><div>Date: 2008-8-20</div><div>Sheet of</div></div> <div><div>File: D:\STS8105系统\...\SH8204.SchDoc</div><div>Drawn By: KB</div></div>
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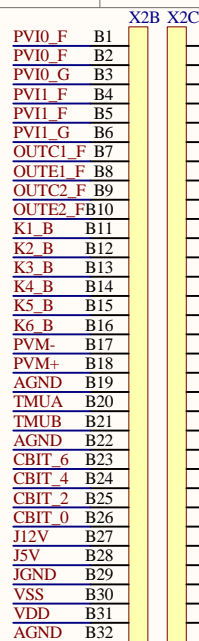
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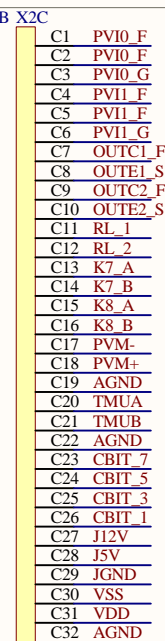
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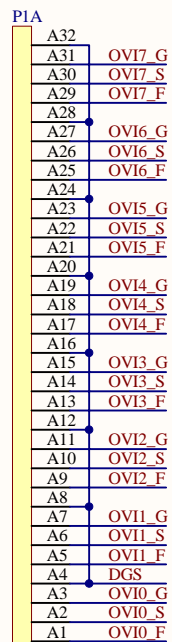
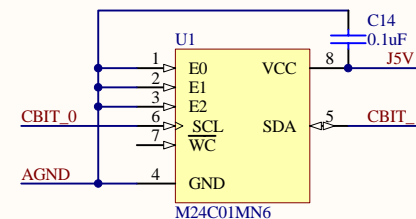
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CZ396-F-S

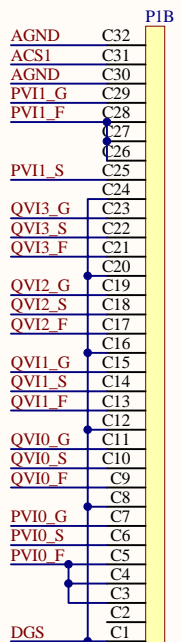


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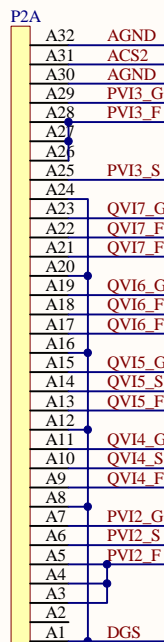


CZ364-M-S

Top Left Receptacle

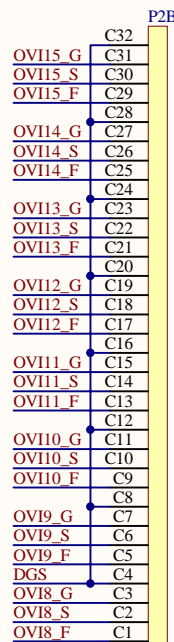


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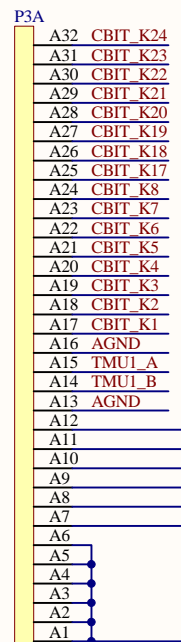


CZ364-M-S

Bottom Left Receptacle

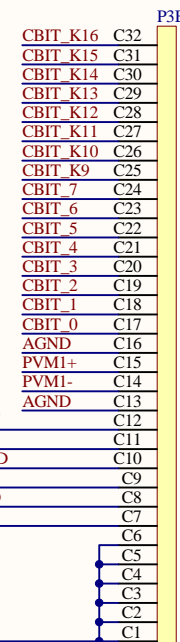


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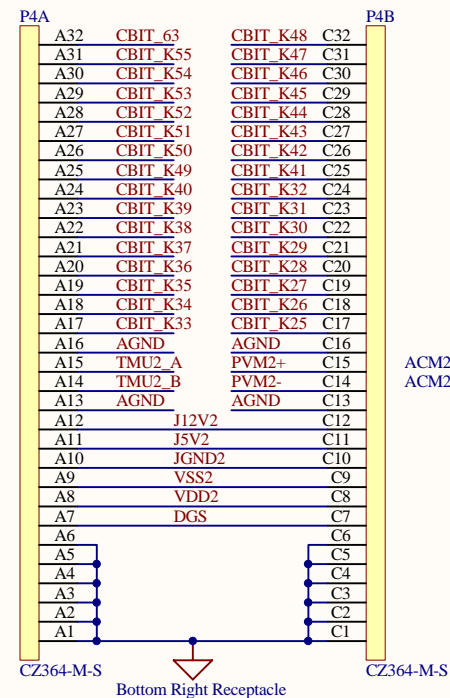


CZ364-M-S

Top Right Receptacle



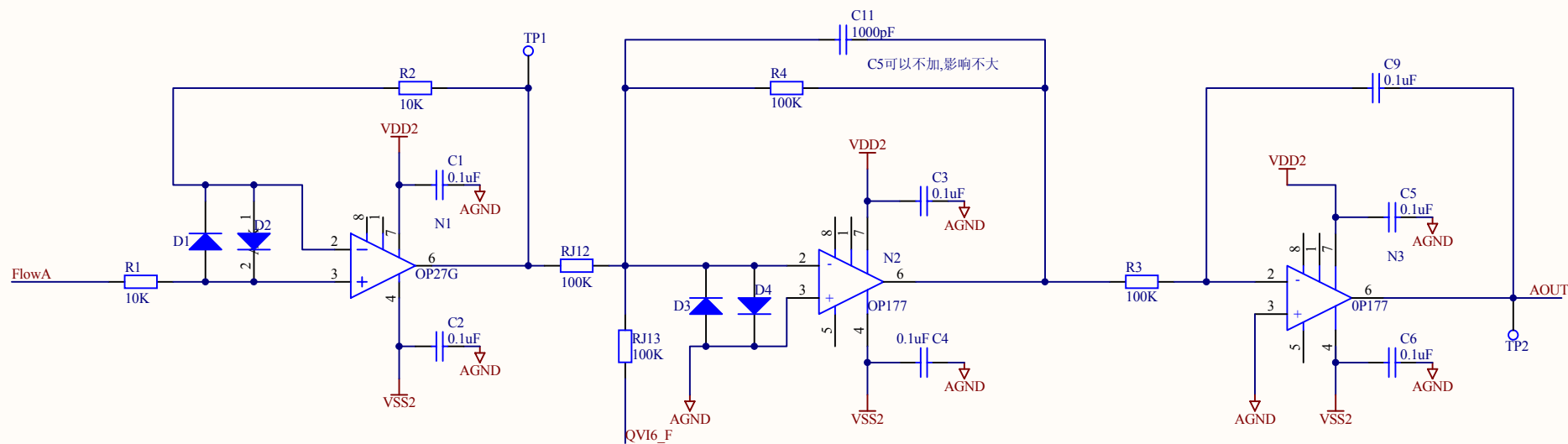
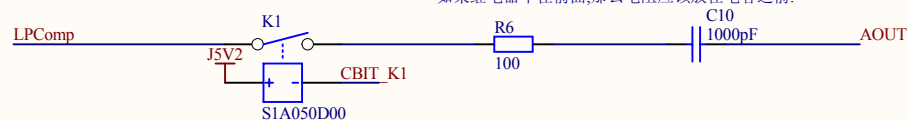
CZ364-M-S

ACM1+
ACM1-ACM2+
ACM2-

Title		
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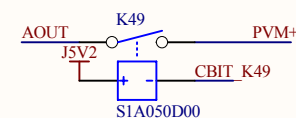
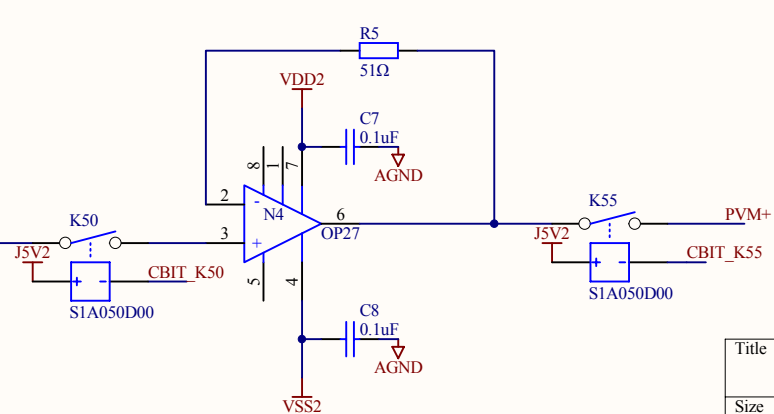
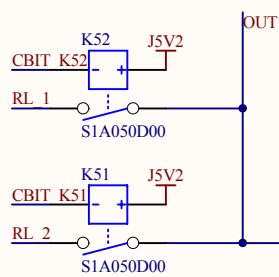
继电器靠前放置,这样可以避免自身闭环时长线引起干扰.

如果继电器不在前面,那么电阻应该放在电容之前.



QVI能出30V,所以电阻比例1:1

负载选择RL1和RL2



Title		
Size A4	Number	Revision
Date:	2009-2-10	Sheet of
File:	D:\STS8105系统\OPL2.SchDoc	Drawn By:

